ABSTRACT OF THE DISCLOSURE

A PWM control circuit, microcomputer and electronic equipment which can generate high-resolution PWM signals
5   through a small-sized scale of circuit. The PWM control circuit includes a PWM period value setting register, a counter, an edge-point value setting register, a PWM output circuit for varying the level of the PWM signal at a first edge-point, and a delay value setting register provided on low order side of
10   the edge-point value setting register, for specifying a delay time of the first edge-point. The PWM output circuit delays the first edge-point by a period smaller than one clock period of CLK, in accordance with the value in the delay value setting register. This can improve the resolution of the PWM signal.
15   One-bit or two-bit value is stored in the delay value setting register. Based on the stored value, the first edge-point can be delayed by 1/2, 1/4, 2/4 or 3/4 clock period.

31